

PATENT
Docket No. INTEL/18682

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant(s): ROTHMAN et al.)	I hereby certify that this paper is
)	being deposited with the United
Serial No.: 10/817,173)	States Postal Service with
)	sufficient postage as first class
Filed: April 2, 2004)	mail in an envelope addressed to:
)	Commissioner for Patents, P.O.
For: "Methods and Apparatus to Enable)	Box 1450, Alexandria, VA 22313-
Code-Based Bus Performance Analysis")	1450 on this date:
)	
Group Art Unit: 2184)	July 30, 2004
)	<u>Frankie Ho</u>
Examiner: Unknown)	Frankie Ho
)	Registration No. 48,479
)	Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC.
20 N. Wacker Drive
Suite 4220
Chicago, Illinois 60606
(312) 580-1020

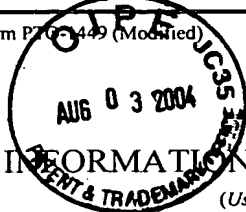
By:



Frankie Ho
Registration No.: 48,479

July 30, 2004

Attorneys for Intel Corporation

Form PTO-449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No.	Serial No.
		INTEL/18682	10/817,173
	Applicant ROTHMAN et al.		
	Filing Date	Group Art Unit	
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		April 2, 2004	2184

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
C01	Silicon Control, Inc., <u>850 System Analyzer/Exerciser</u> , http://www.silicon-control.com/PCI850%20data%20sheet.pdf .	
C02	Silicon Control, Inc., <u>PCI850 – Advanced PCI Bus Analyzer/Exerciser</u> , http://www.silicon-control.com/PCI850%20Product%20Sheet.pdf .	
C03	Silicon Control, Inc., <u>PCI850 Users Manual</u> , 16, 49-55 (May 2003), http://www.silicon-control.com/PCI850%20Users%20Guide.pdf .	
C04	Silicon Control, Inc., <u>PCI-X Bus Analyzer</u> , http://www.silicon-control.com/pci8520.htm .	

EXAMINER	DATE CONSIDERED
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.</p>	